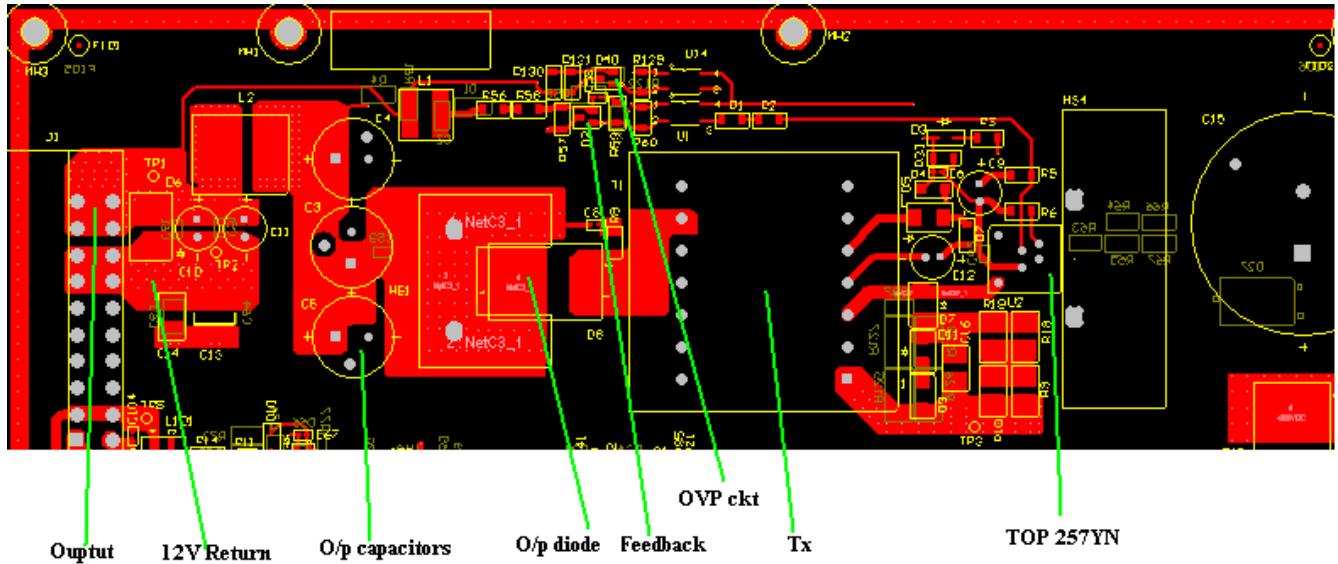


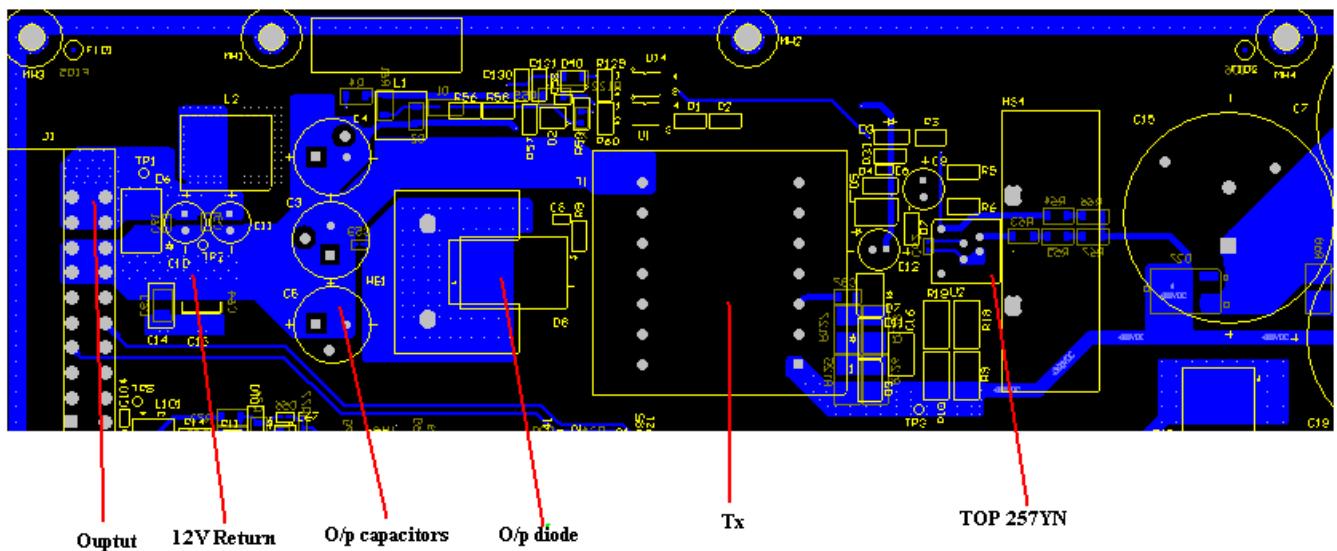
Date: 21-Feb-2012

12V @10A converter layout (4-layer PCB)

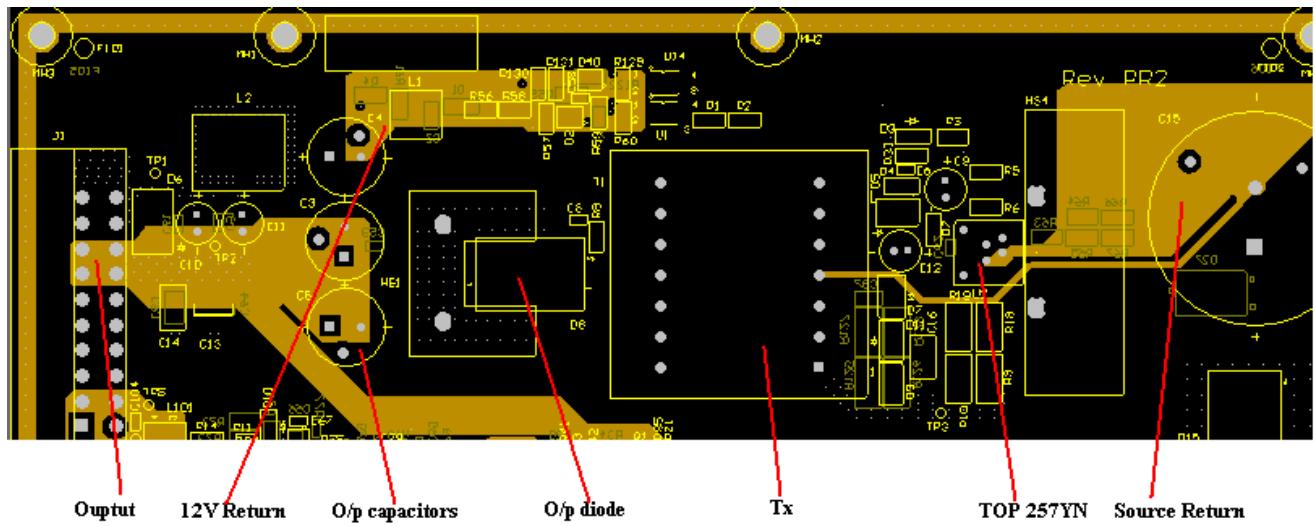
1. TOP LAYER:



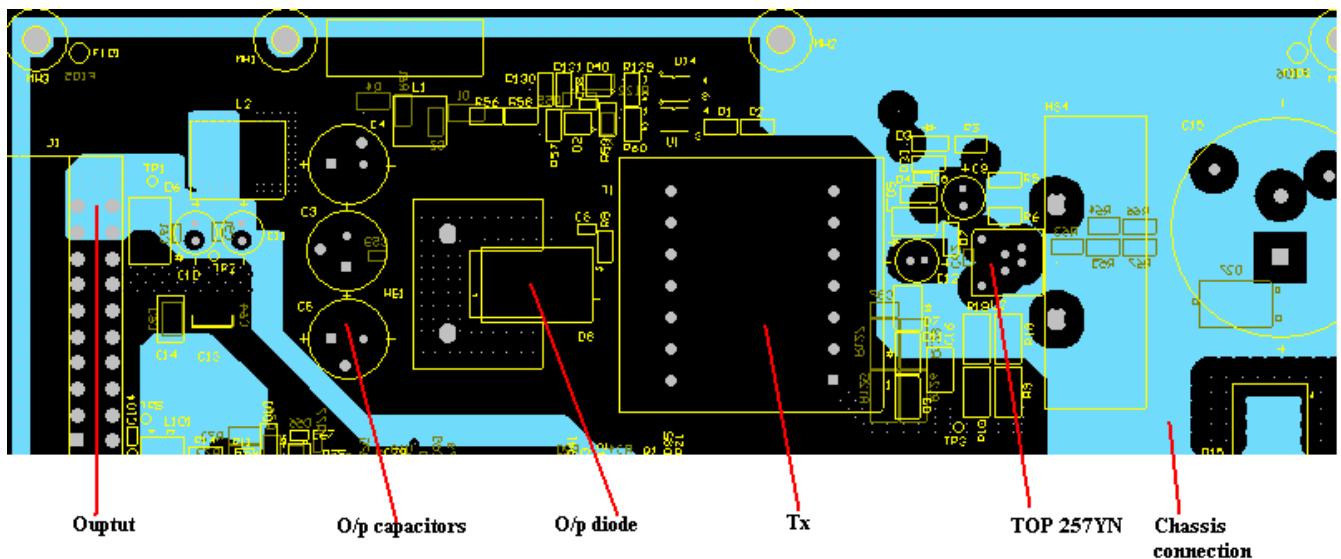
2. BOTTOM LAYER:



3. 2nd Layer (from Top):



4. 3rd Layer (from Top):



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